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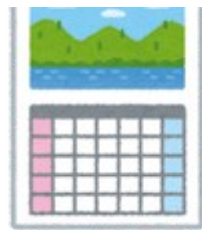


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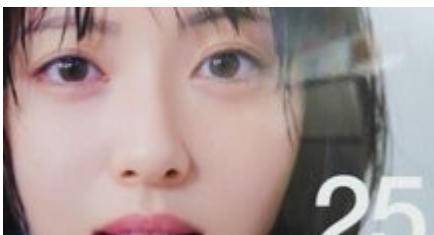


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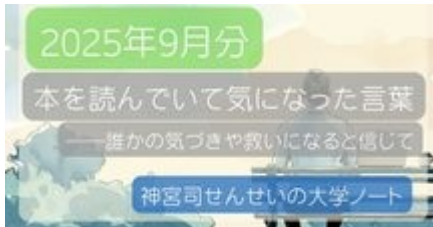
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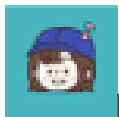


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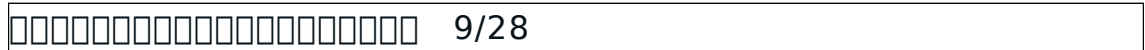
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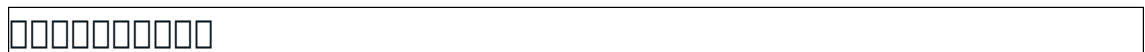
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A diagram of a 4-bit shift register. It consists of a horizontal rectangle divided into four equal-sized square cells. Above the rectangle, there are four input labels: A, B, C, and D, each aligned with one of the cells. Below the rectangle, there are four output labels: Q1, Q2, Q3, and Q4, each aligned with one of the cells. A horizontal line runs along the bottom of the rectangle, with a small vertical tick mark at the right end, indicating a serial output or clock input.



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A diagram of a 4-bit shift register. It consists of four rectangular cells arranged horizontally. The leftmost cell has two inputs labeled 'A' and 'B'. The other three cells each have one input labeled 'C', 'D', and 'C' respectively. The rightmost cell has two outputs labeled 'Q3' and 'Q2'. The other three cells each have one output labeled 'Q1', 'Q0', and 'Q3' respectively. The cells are connected in a chain, with the output of one cell being the input to the next cell.

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